

Notice of Allowability

Application No.

09/760,560

Examiner

Lawrence B Williams

Applicant(s)

CHIU, KENNY KOK-HOONG

Art Unit

2634

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to After Final Amendment filed on 21 November 2005.
2. ☒ The allowed claim(s) is/are 1, 4-9, 11-14, 17-22, 25-28, 30, 32-34, renumbered as 1, 2-7, 8-11, 12-17, 18-21, 22, 23-25, respectively.
3. ☒ The drawings filed on 11 April 2001 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
 - * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance: The instant application discloses a clock selecting device and method. A search of prior art records has failed to teach a device comprising; "a clock synchronization logic coupled to the first input clock line, the second input clock line, and the clock selection logic, adapted to synchronize the first input clock line, the second input clock line, and the clock selection logic, such that the internal clock line is glitch free, the clock synchronization logic triggering the clock selection logic to select the first input clock line and the second input clock line, the clock synchronization logic comprising: an OR gate coupled to a clock select line and an internal feedback line of the clock synchronization logic; a first plurality of flip-flops coupled to the output of the OR gate and the second input clock line, producing a clock switch line adapted to cause the clock selection logic to switch between the first clock source and the second clock source; an AND gate coupled to the clock select line and the clock switch line; and a second plurality of flip-flops coupled to the output of the AND gate and the first input clock line, the output of the second plurality of flip-flops coupled to the internal feedback line" " as disclosed in claims 1 and 14. Nor does the prior teach a method of selecting one of a pair of a clock sources onto a single output clock line, comprising the steps of: "(d) synchronizing the first input clock signal, the second input clock signal, and step (c), such that the output clock line is glitch free, comprising the steps of: ORing a clock select signal and an internal feedback signal; producing a clock switch signal adapted to trigger the performance of step (c), as a result of the step of Oring the clock select signal and the internal feedback signal; ANDing the clock select signal and the clock switch signal; and

coupling the result of the step of ANDing the clock select signal and the clock switch signal to the internal feedback signal” as disclosed in claim 28.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

CONCLUSION

2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lawrence B Williams whose telephone number is 571-272-3037. The examiner can normally be reached on Monday-Friday (8:00-5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner’s supervisor, Kenneth Vanderpuye can be reached on 571-272-3078. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Lawrence B. Williams

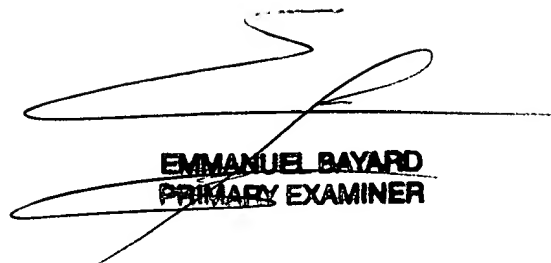
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lbw

November 28, 2005



EMMANUEL BAYARD
PRIMARY EXAMINER